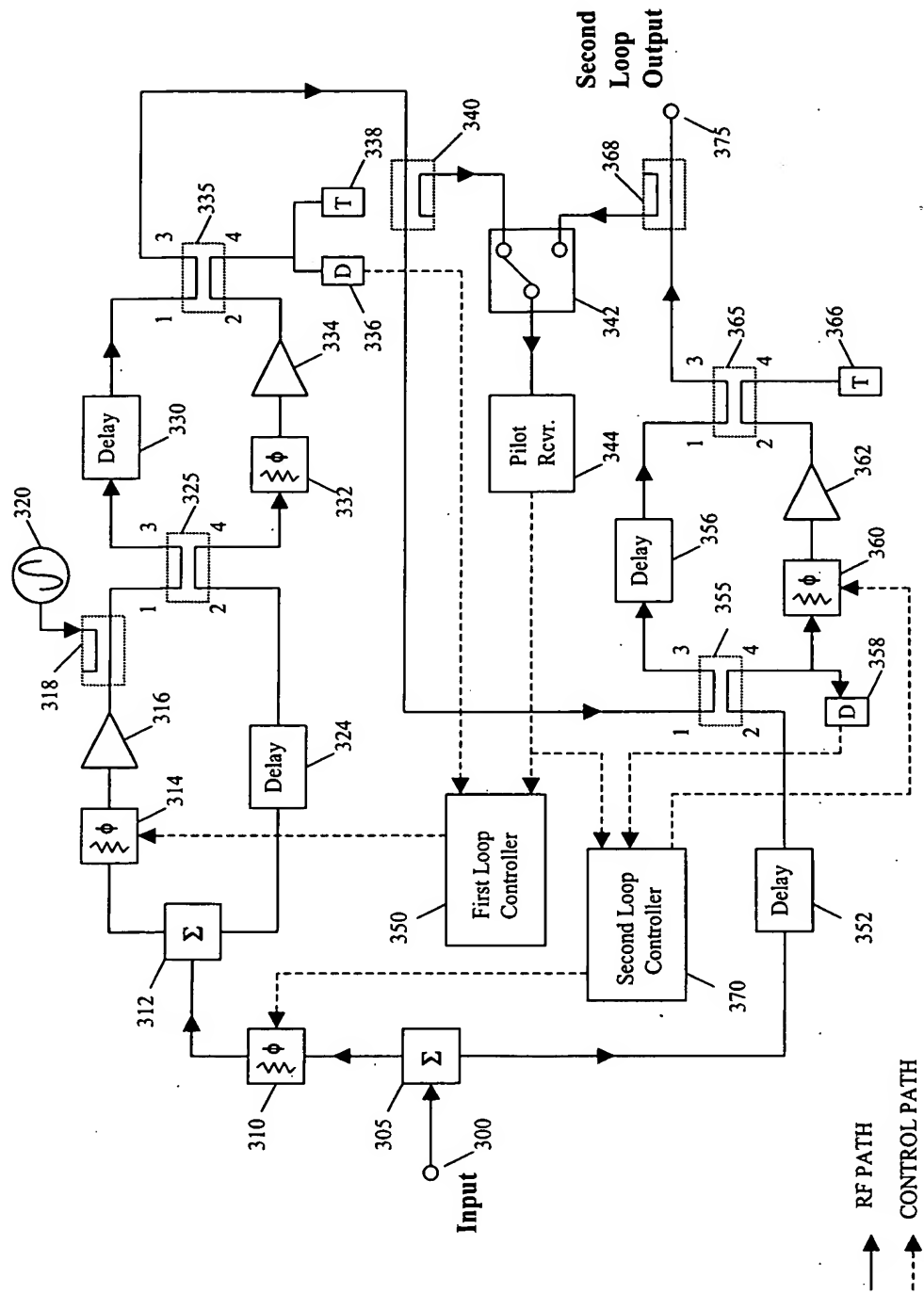


PRIOR ART

FIG. 1

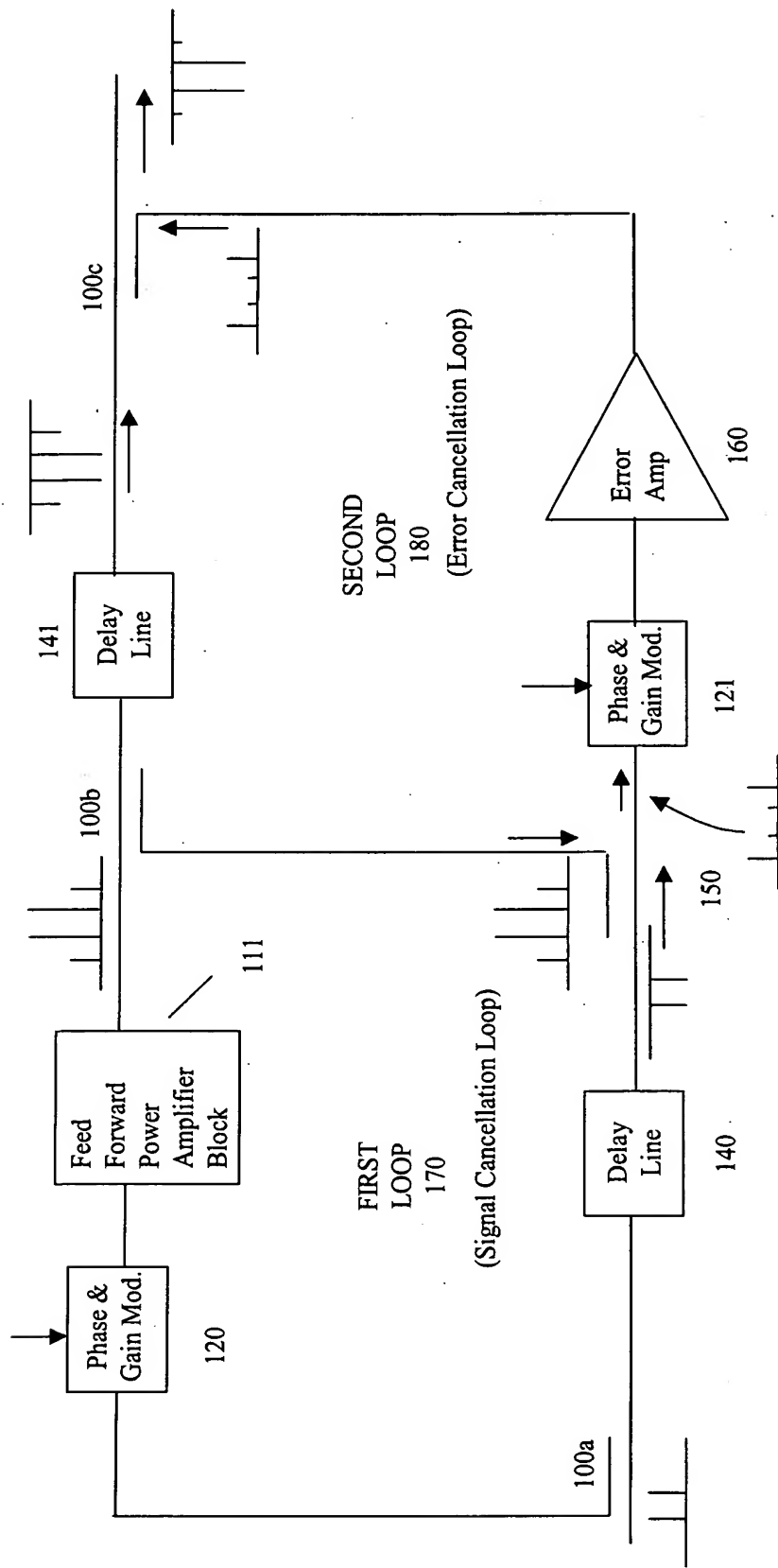




PRIOR ART

FIG. 2

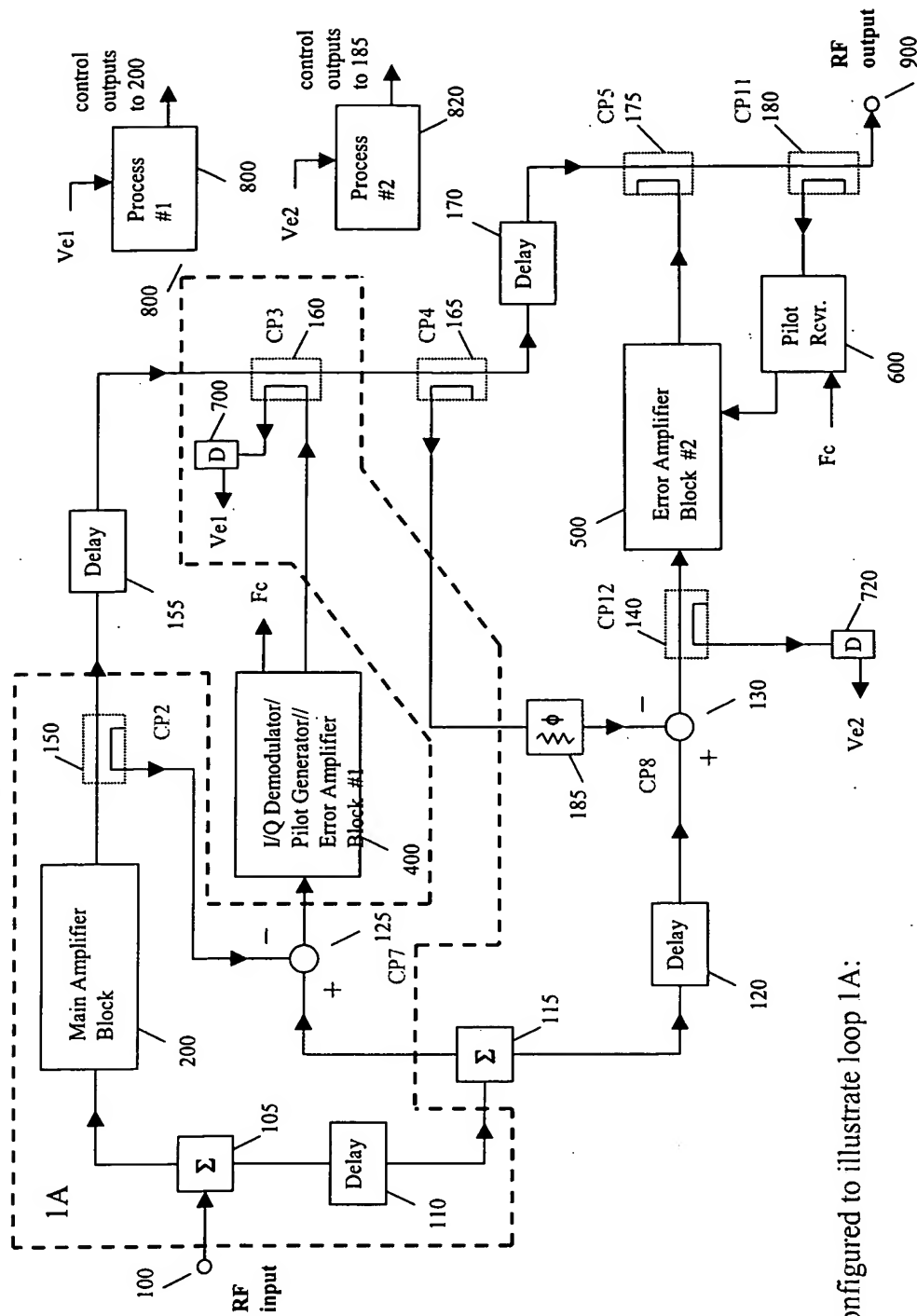




# PRIOR ART

FIG. 3





configured to illustrate loop 1A:

FIG. 4



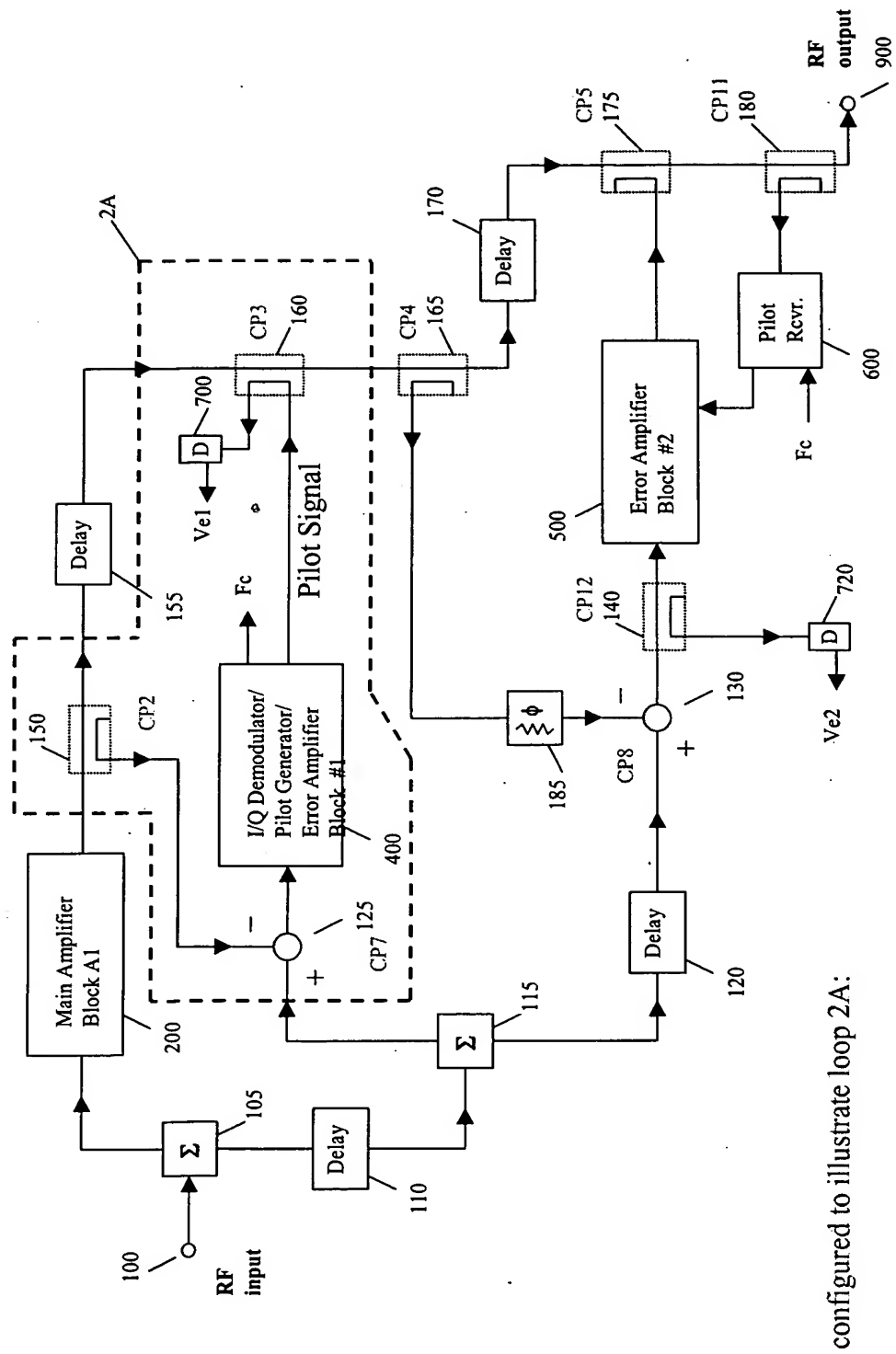
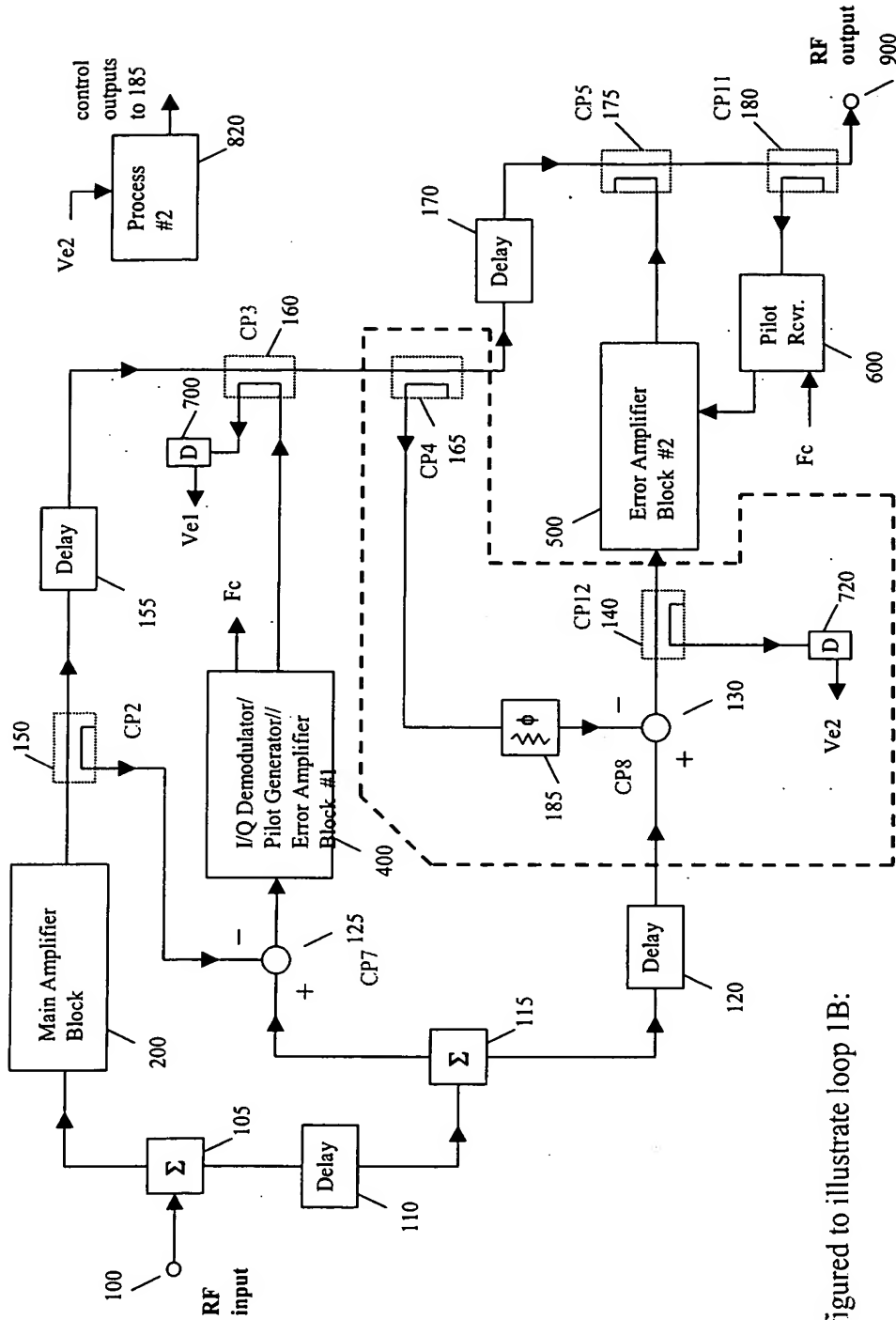


FIG. 5





configured to illustrate loop 1B:

FIG. 6







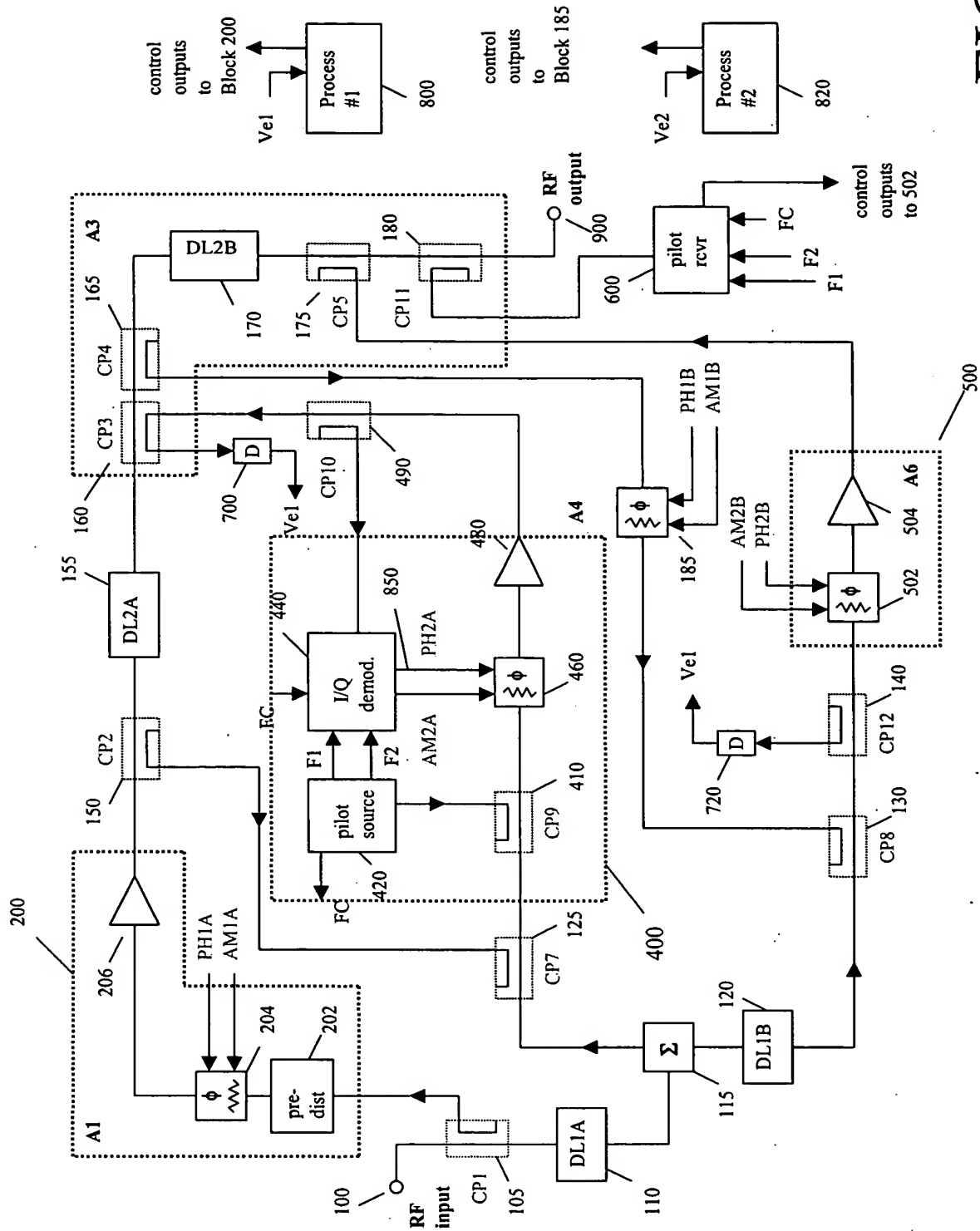


FIG. 8



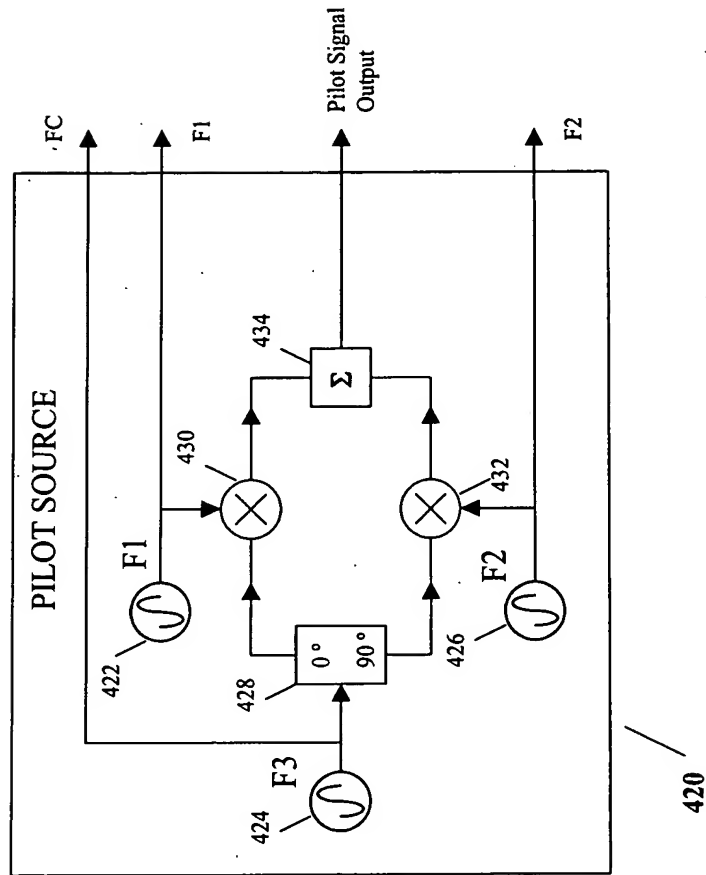


FIG. 9



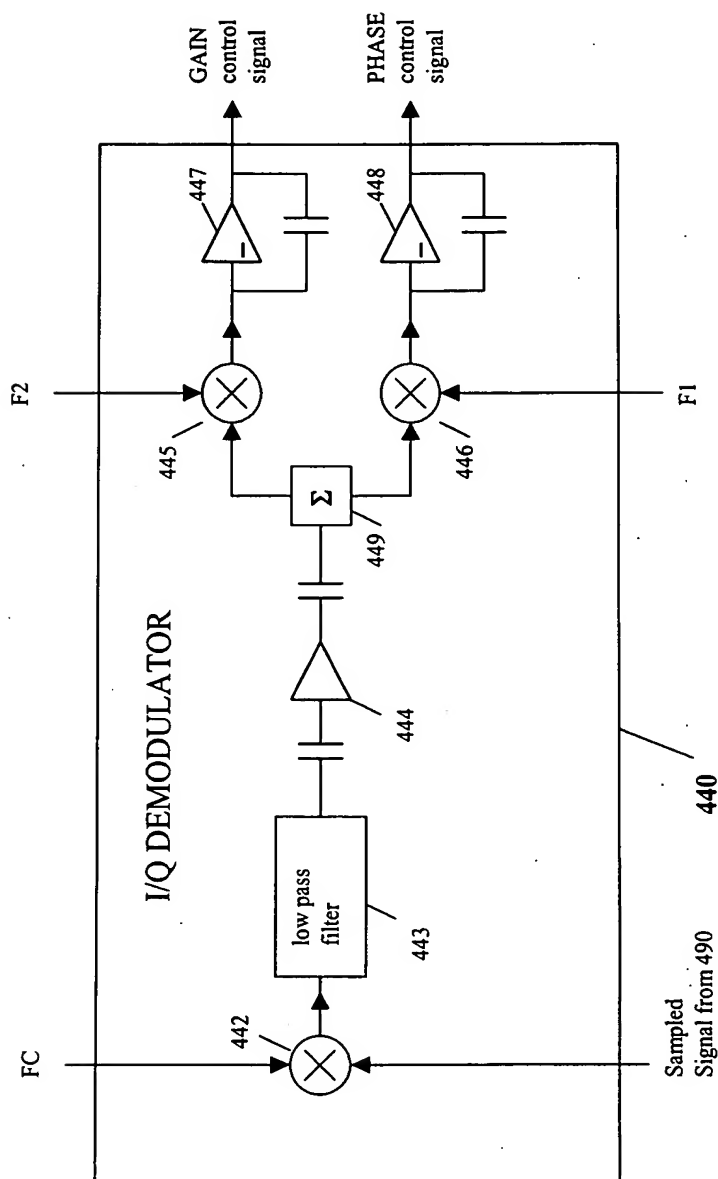


FIG. 10



Configured to illustrate output coupler loss minimization loop only:

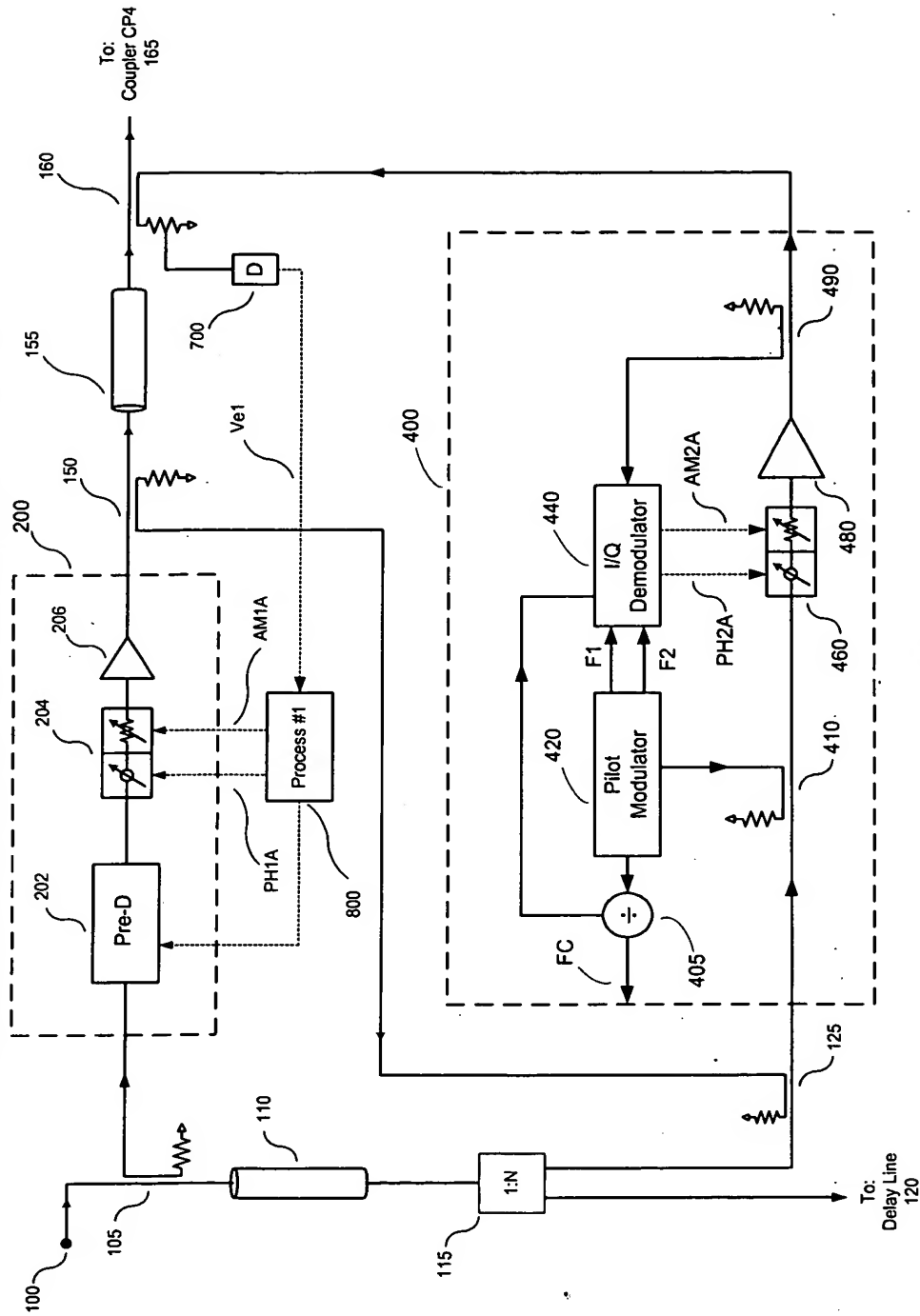


FIG. 11



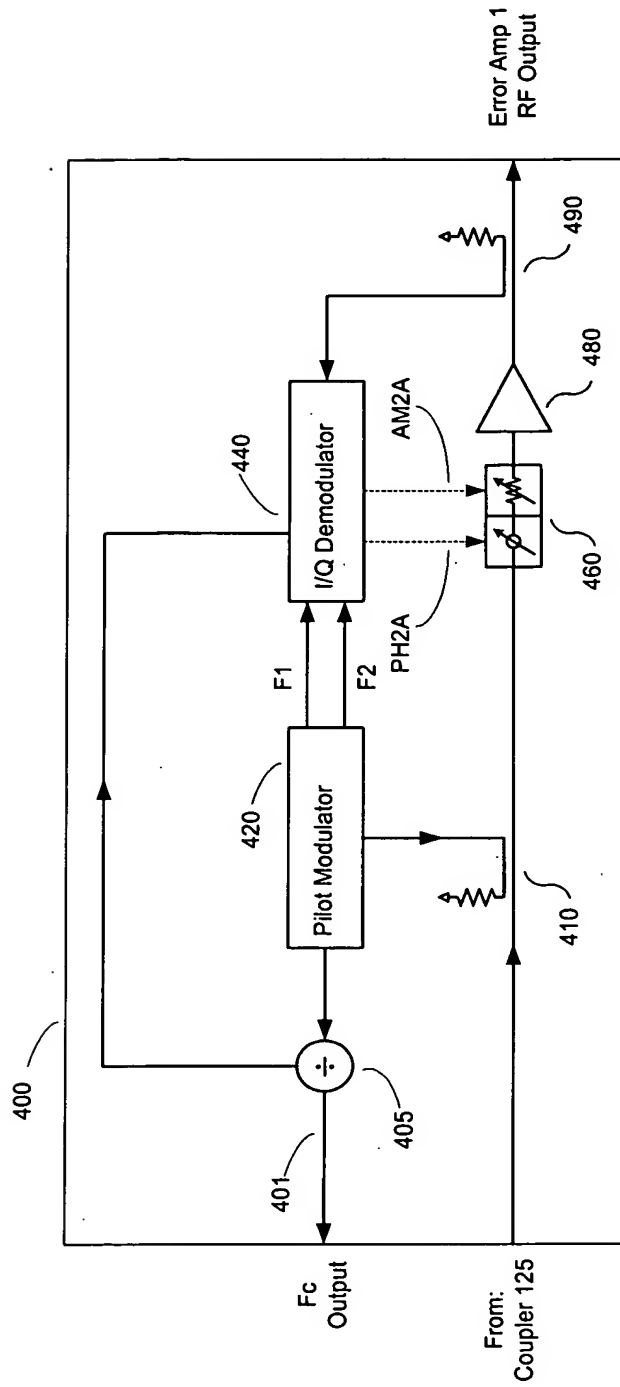
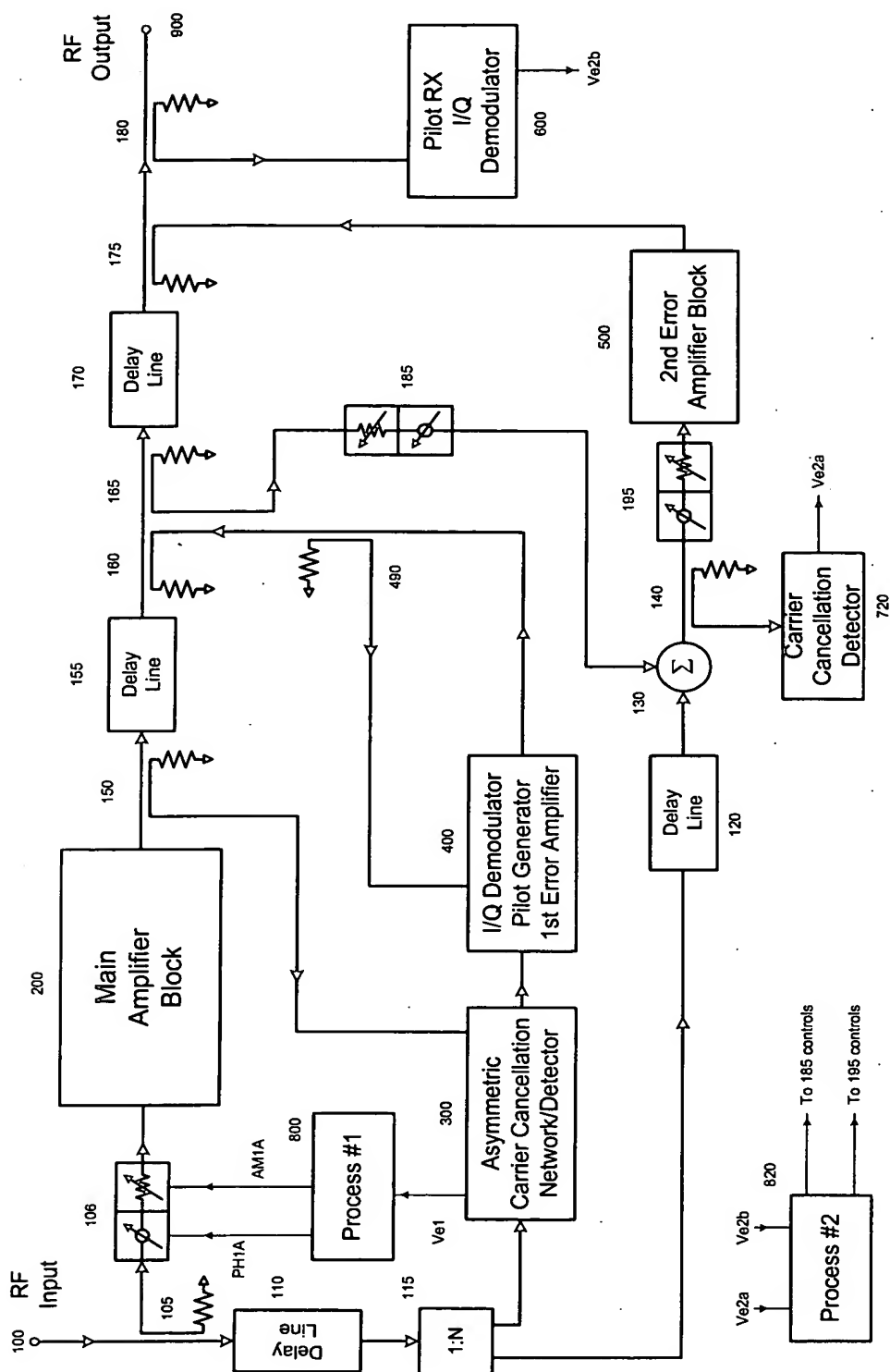


FIG. 12







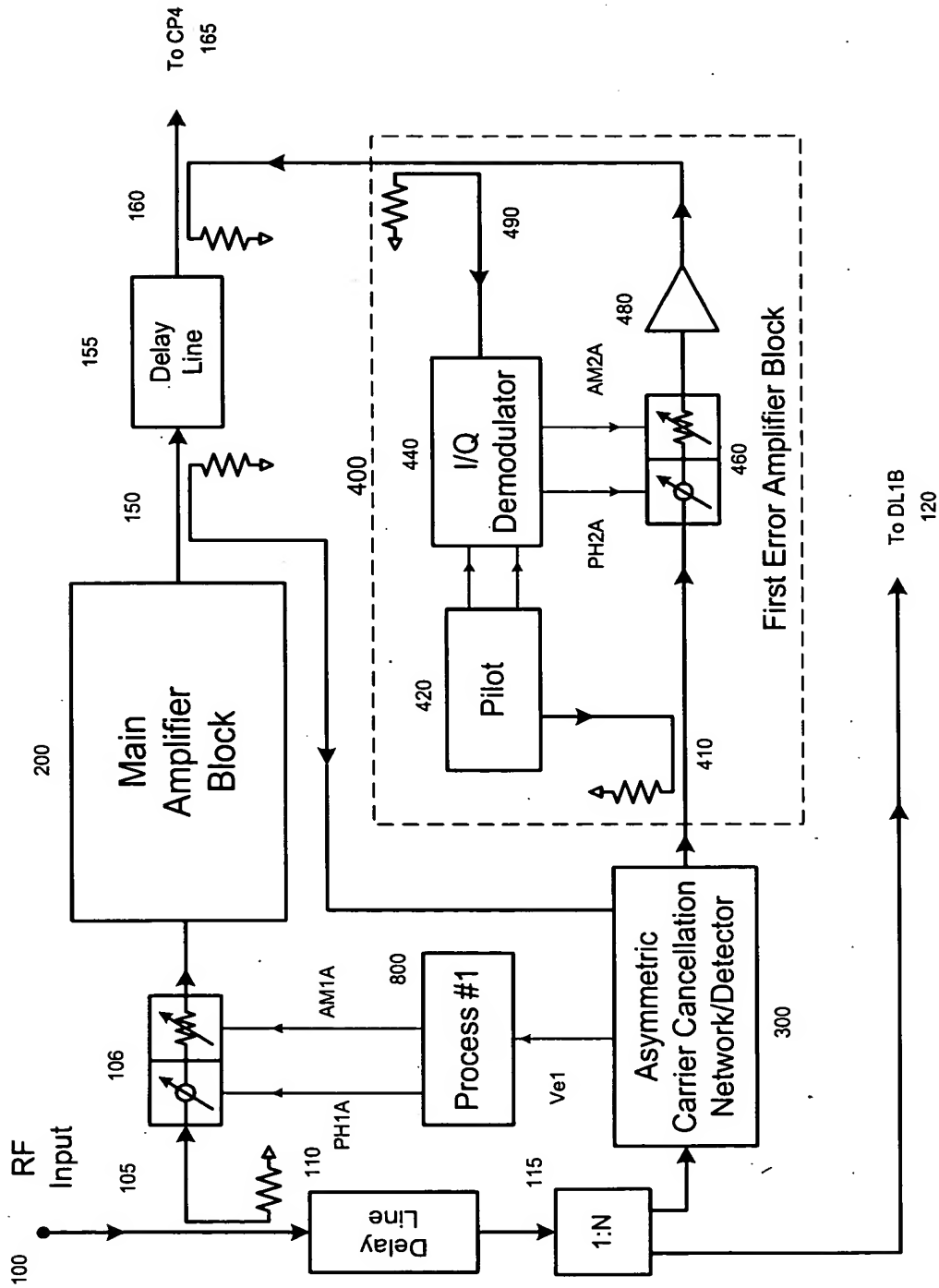


FIG. 13B



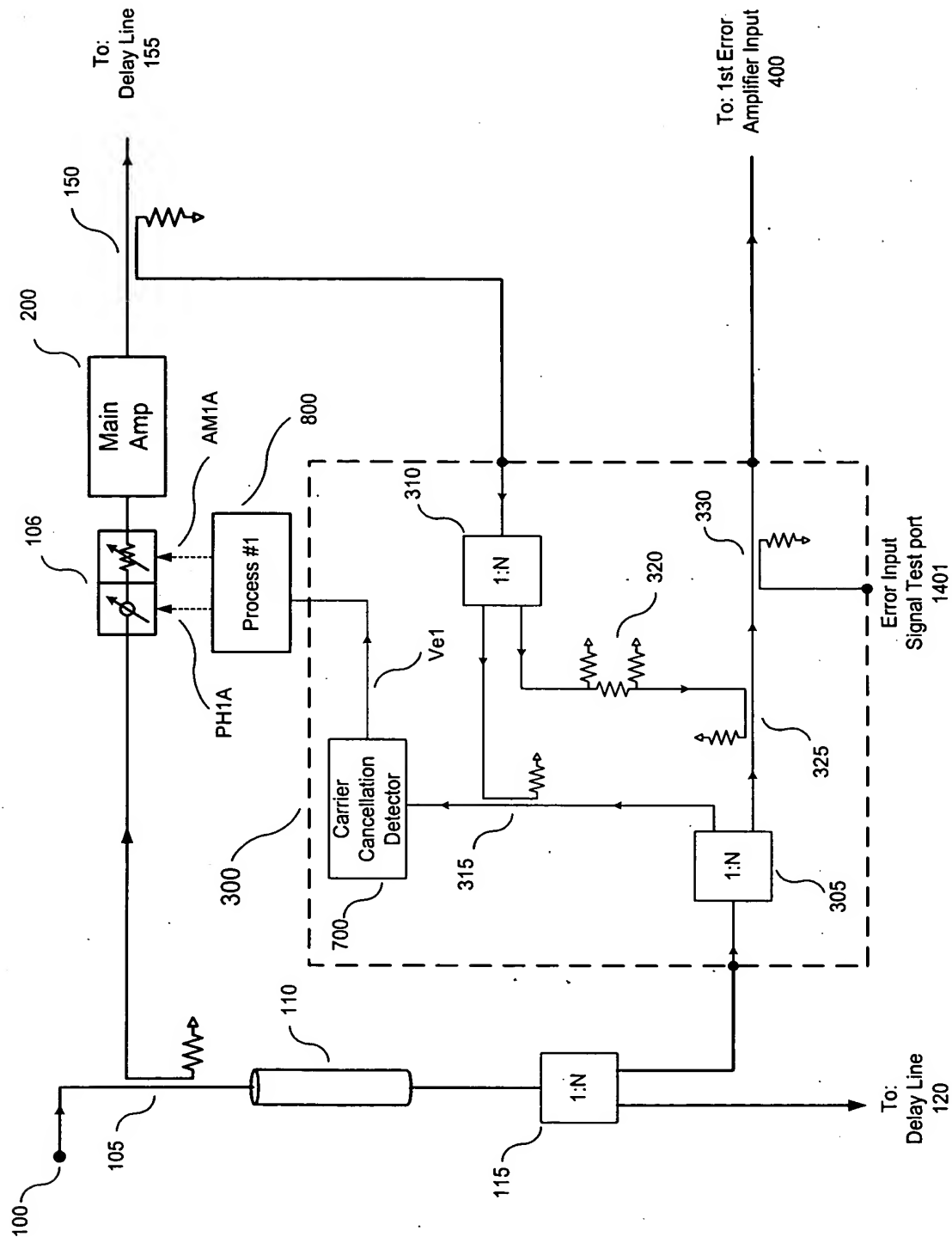


FIG. 14







# Error Amplifier Stabilization

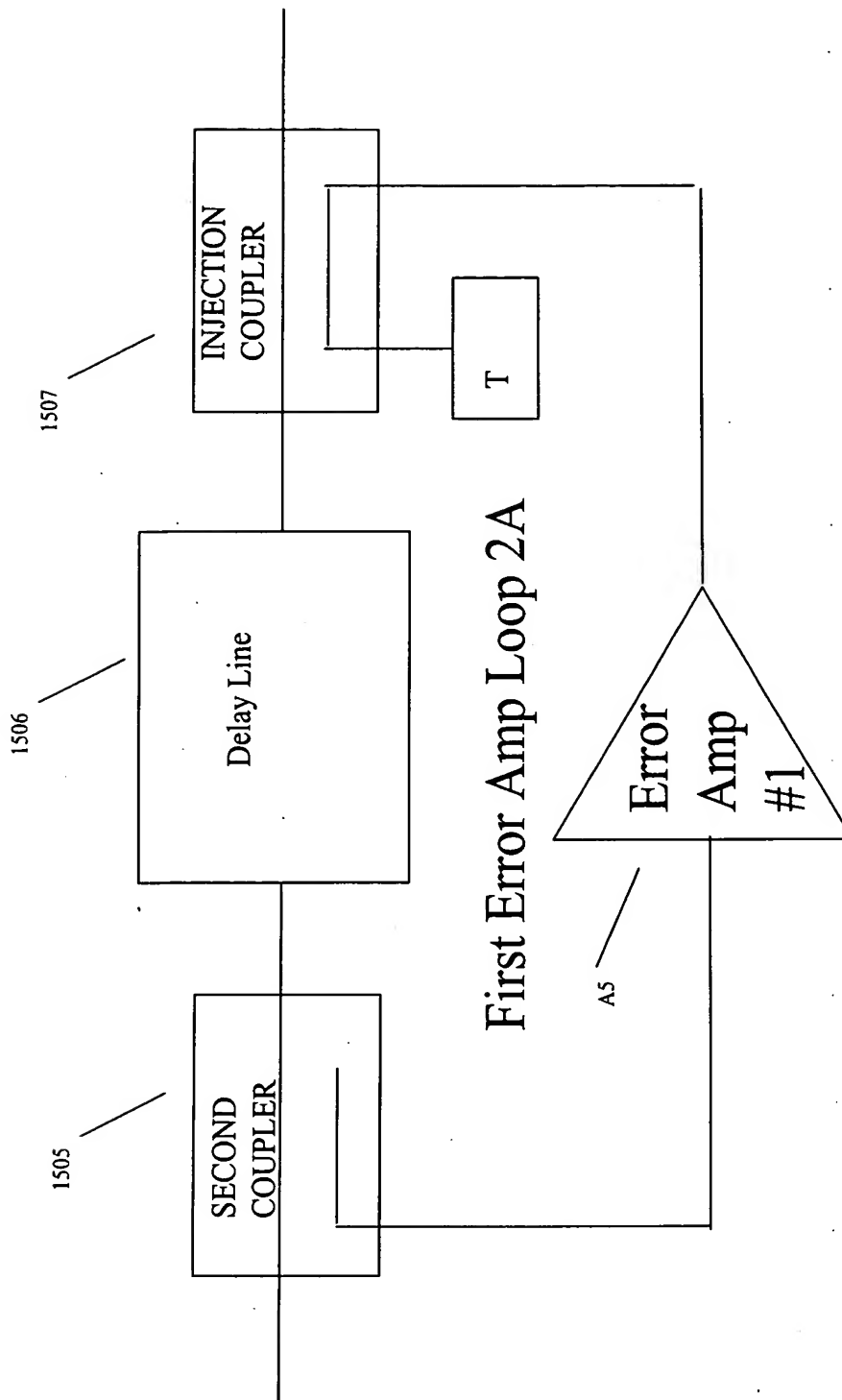


FIG. 16



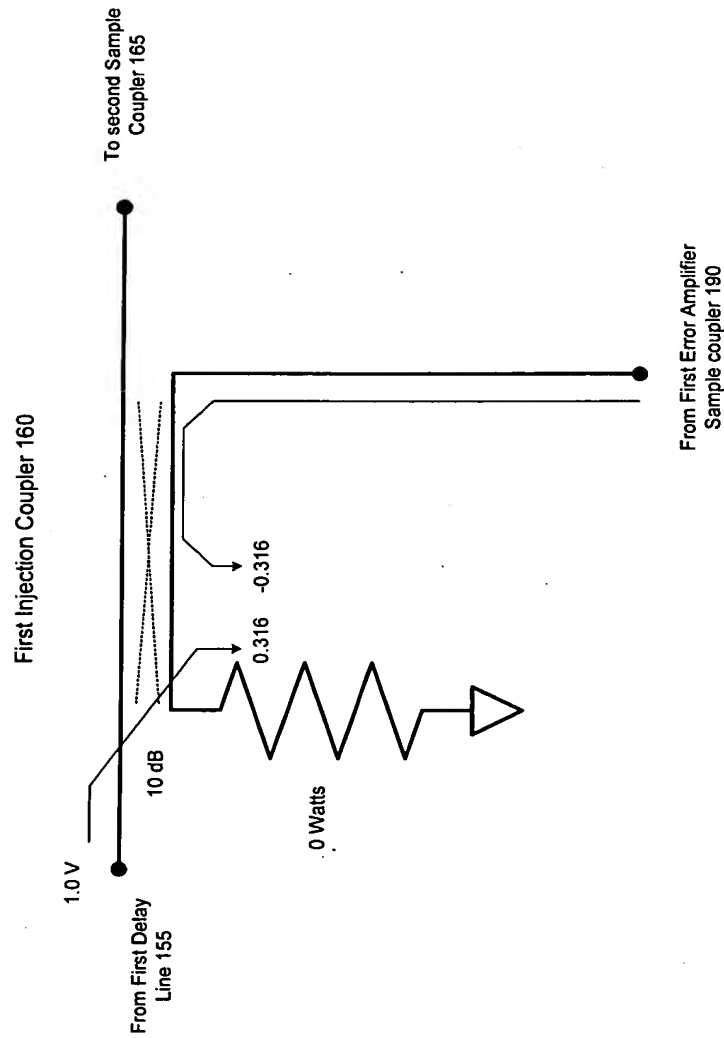


FIG. 17







